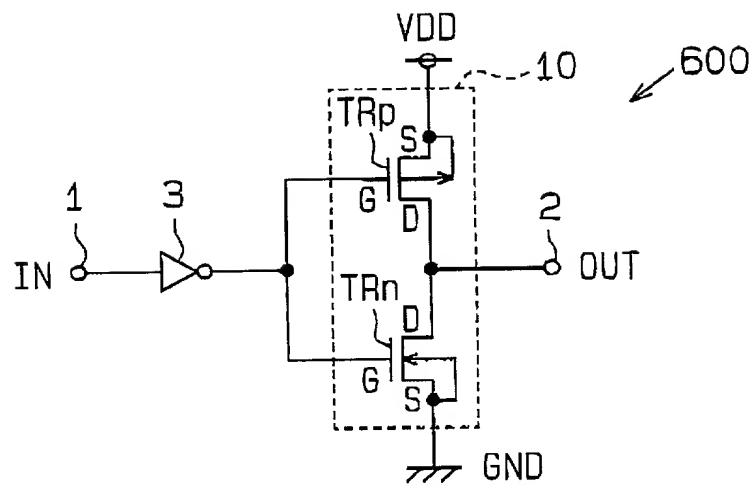
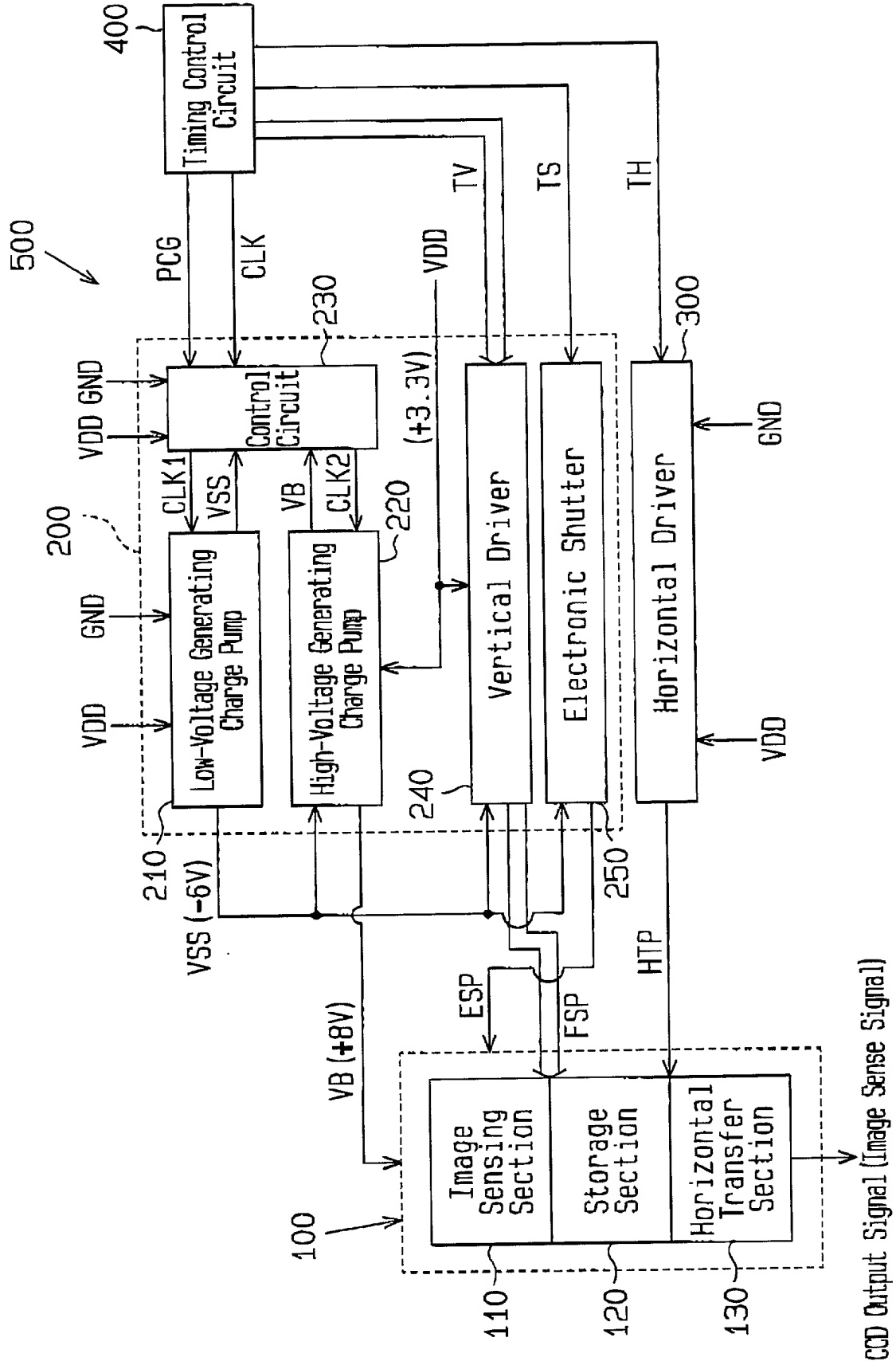
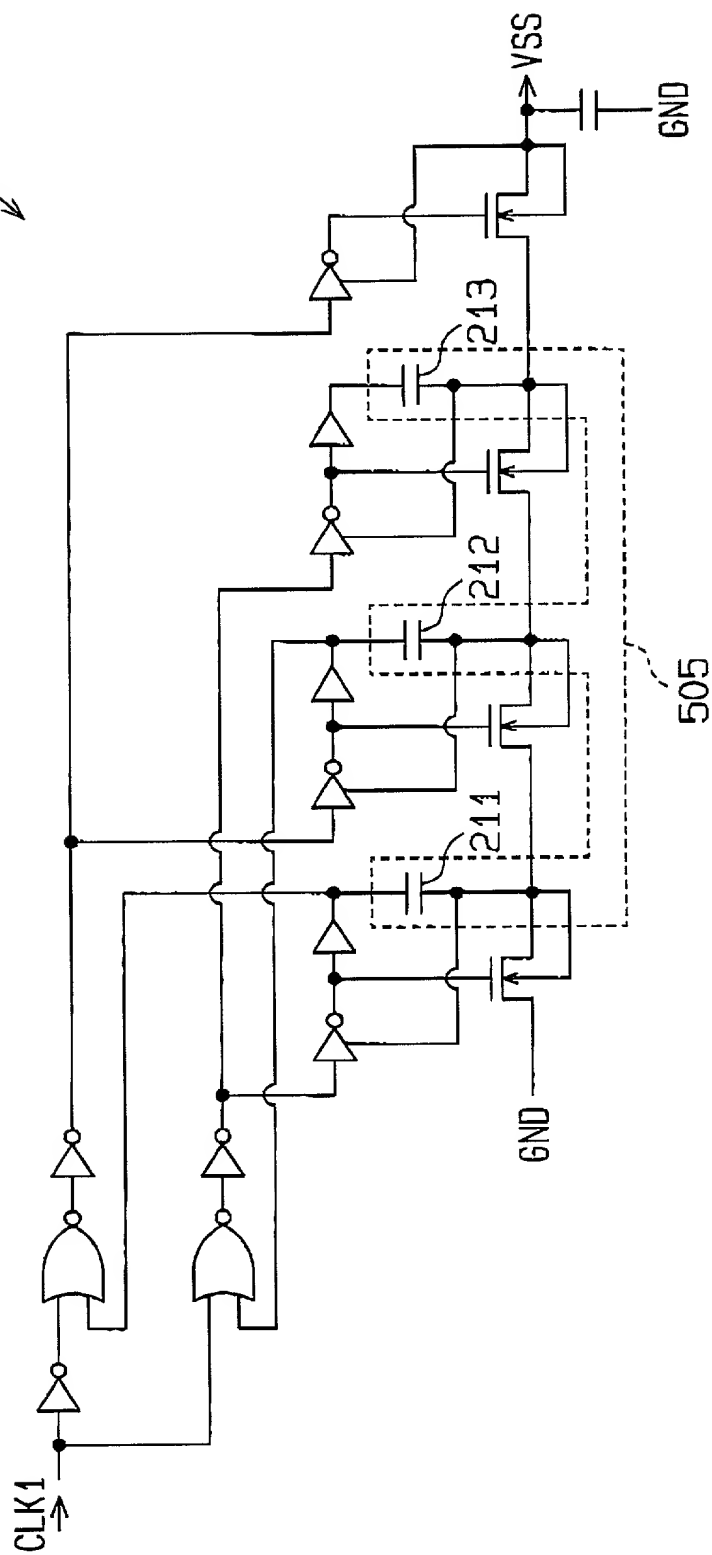


**Fig.1 (Prior Art)**



# Fig. 2





**Fig.3**

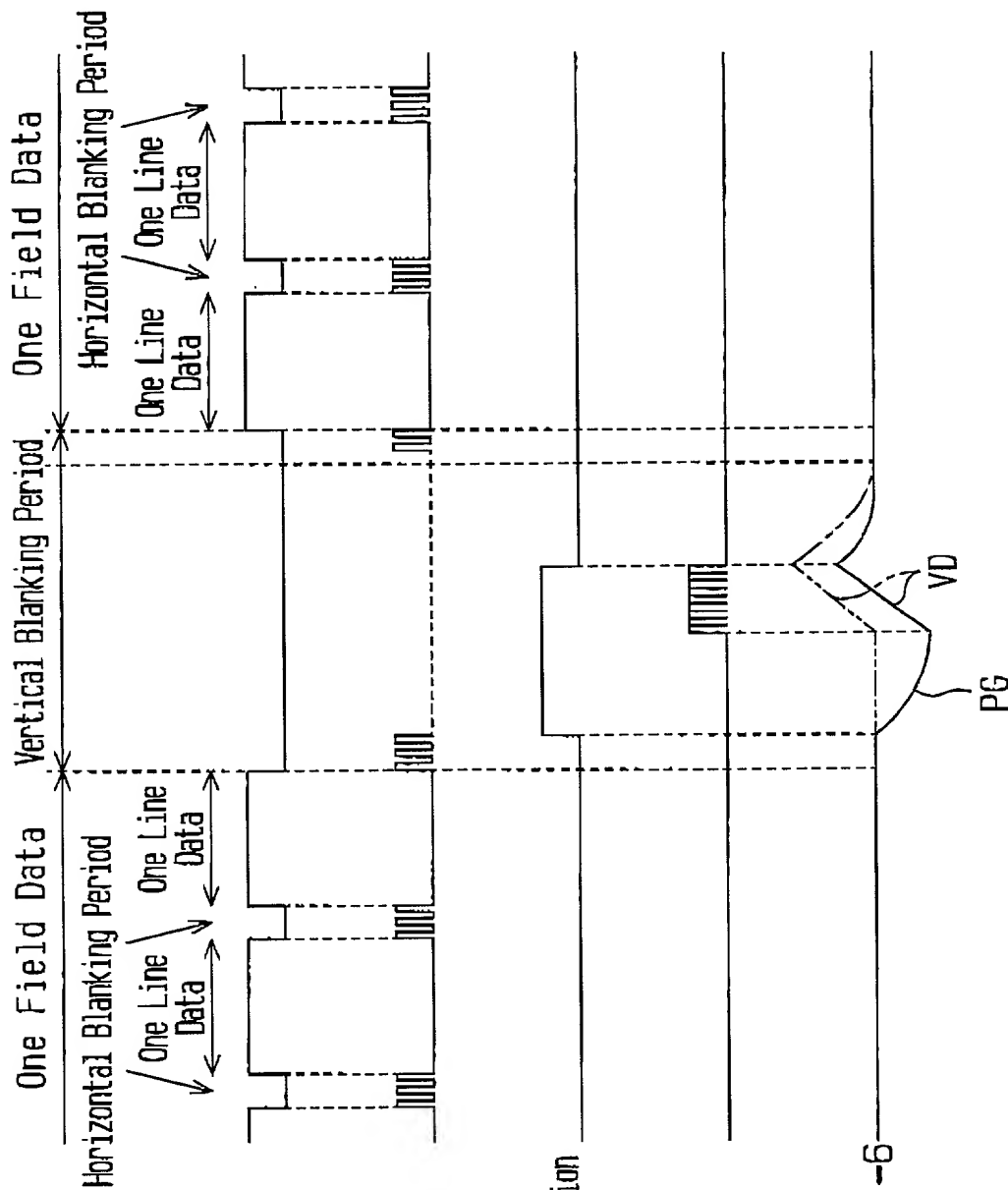
**Fig.4(a)** Image Sense Signal

**Fig.4(b)** Boost Clock Signal  
CLK1

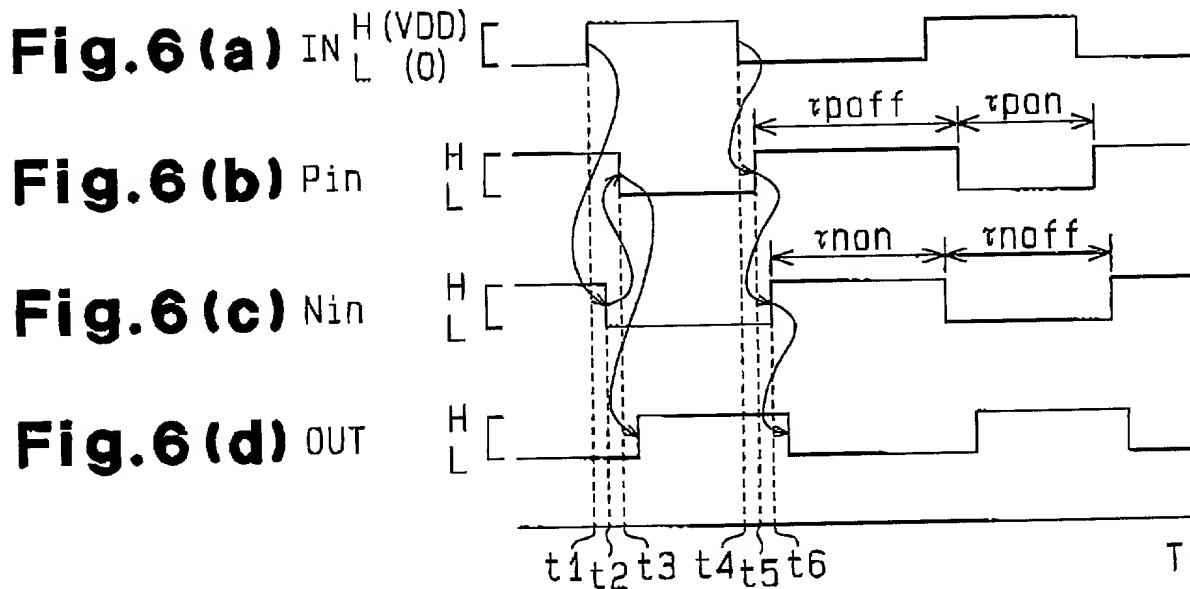
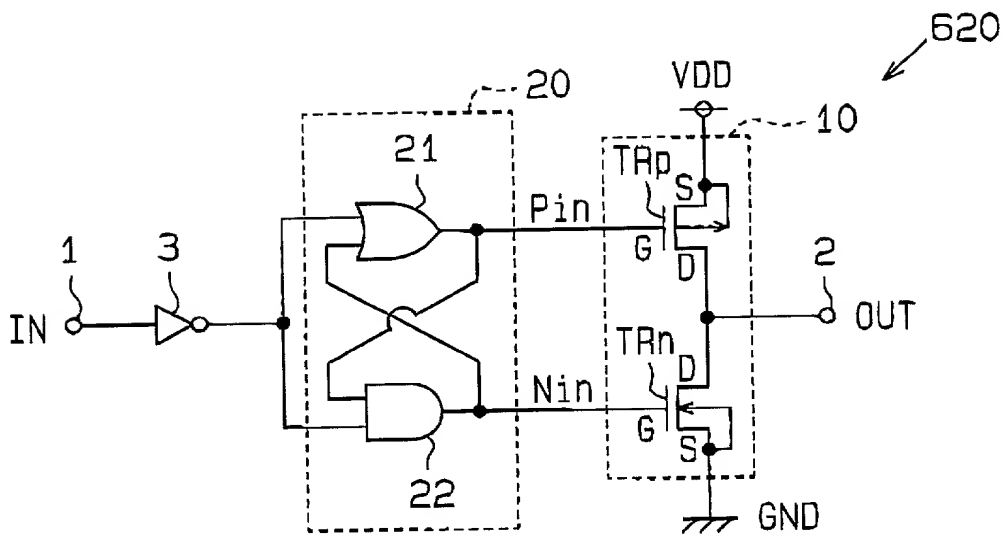
**Fig.4(c)** Precharge Instruction  
Signal POG

**Fig.4(d)** Frame Shift Pulse  
Signal FSP

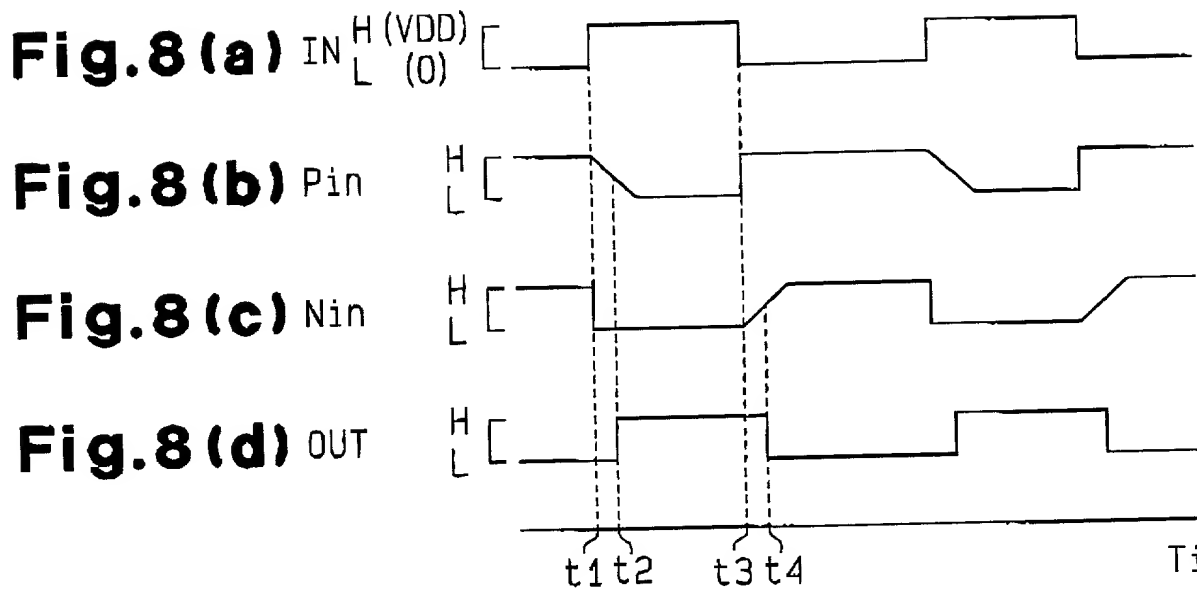
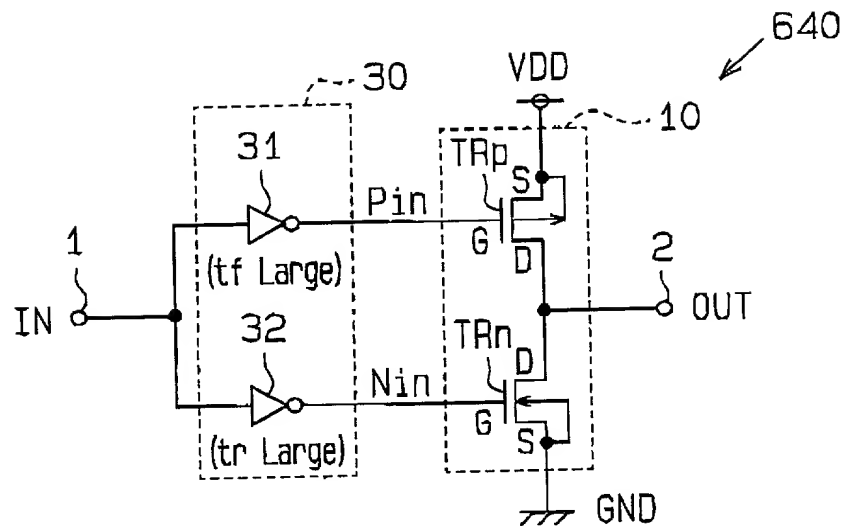
**Fig.4(e)** VSS



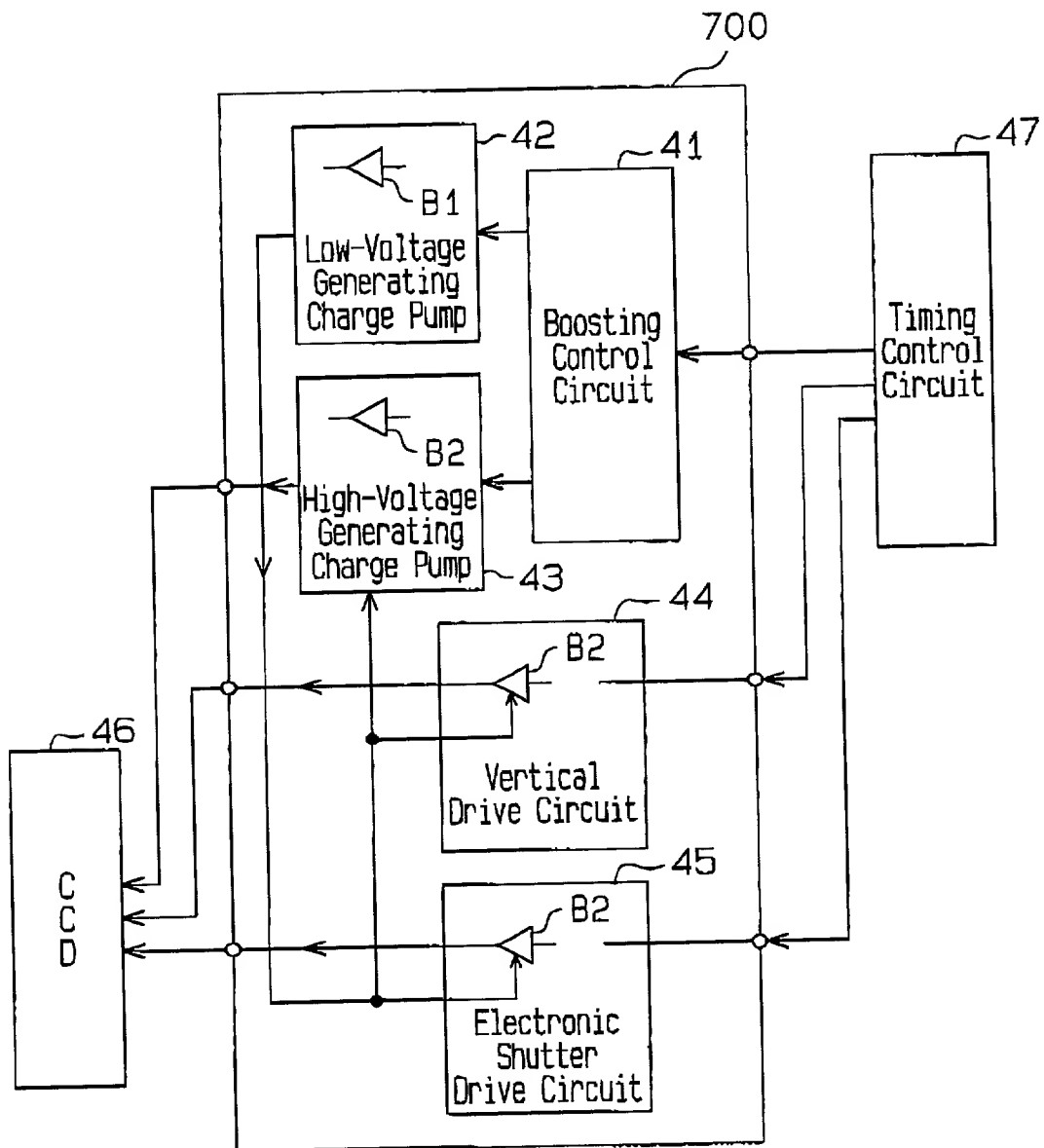
**Fig.5**



# Fig.7



**Fig.9**



**Fig.10**

